



Attorney's Docket No.: 104-001 / F51-142893M/TOM

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Electon  
11/15/02  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yoshinori Hino et al.  
Serial No. : 10/076,154  
Filed : February 14, 2002  
Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Art Unit : 2826  
Examiner : Tan N. Tran

Commissioner for Patents  
Washington, D.C. 20231

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RESPONSE TO RESTRICTION REQUIREMENT

Responsive to the action mailed October 8, 2002, applicant elects the invention of Group III (claims 18 to 23) drawn to a semiconductor device. Claim 1 is deemed to be generic and therefore should be included in Group III. The election is made without traverse.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Chris T. Mizumoto  
Reg. No. 42,899

Date: 11/6/02  
Fish & Richardson P.C.  
45 Rockefeller Plaza, Suite 2800  
New York, New York 10111  
Telephone: (212) 765-5070  
Facsimile: (212) 258-2291

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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